TOSHIBA MOS MEMORY PRODUCTS

8192 WORD x 8 BIT CMOS RAM

SILICON GATE CMOS

TC5564P-15
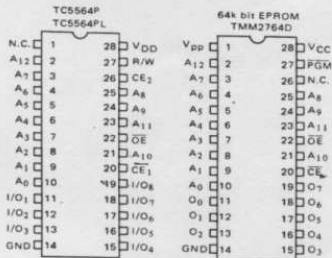# TC5564PL-15

DESCRIPTION

The TC5564P is a 65,536-bit high speed static random access memory organized as 8,192 words by 8 bits using CMOS technology, and operates from a single 5-volt supply.

The TC5564P features output enable and chip enable inputs, that is, $\overline{OE}$ for fast memory access and $\overline{CE}_1$, CE_2 for a minimum standby current mode. So it is suited for a high speed, and low power applications where battery operation and/or battery back up for nonvolatility are required. The TC5564P is guaranteed for voltage as low as 2.0 volt. Furthermore the TC5564PL is guranteed a standby current equal

to or less than $1\mu A$ at Ta = 60°C ambient temperature available.

The TC5564P also features pincompatibility with the 64K bit EPROM (TMM2764D). This means that the TC5564P and EPROM can be interchanged in the same socket, and flexibility in the definition of the quantity of RAM versus EPROM obtained as a result allows the wide application in microcomputer system.
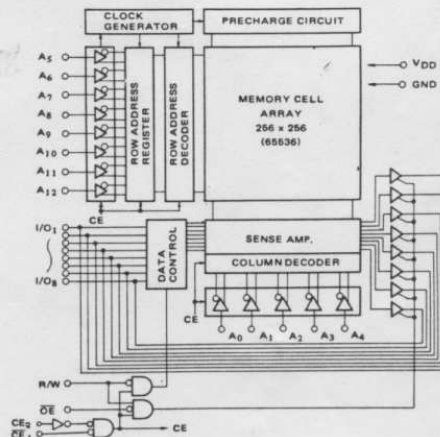
The TC5564P is moulded in a dual-in-line 28 pin standard plastic package, 0.6 inch width.

FEATURES

- Low Standby Current
  - 0.2μA (Max.) at Ta = 25°C
  - 1.0μA (Max.) at Ta = 60°C } TC5564PL-15
  - 20 μA (Max.) at Ta = 85°C } TC5564P-15
- Low Power Dissipation
  - 27.5mW/MHz (Max.) Operating
- 5V Single Power Supply
- 8,192 Word x 8 Bit
- Fully Static Operation
- Data Retention Supply Voltage: 2.0 ~ 5.5V

PIN CONNECTION (TOP VIEW)

PIN NAMES

$A_0 \sim A_{12}$	Address Inputs
R/W	Read/Write Control Input
$\overline{OE}$	Output Enable Input
$\overline{CE}_1$, CE_2	Chip Enable Inputs
$I/O_1 \sim I/O_8$	Data Input/Output
V_{DD}	Power (+5V)
GND	Ground
N.C.	No Connection

- Access Time

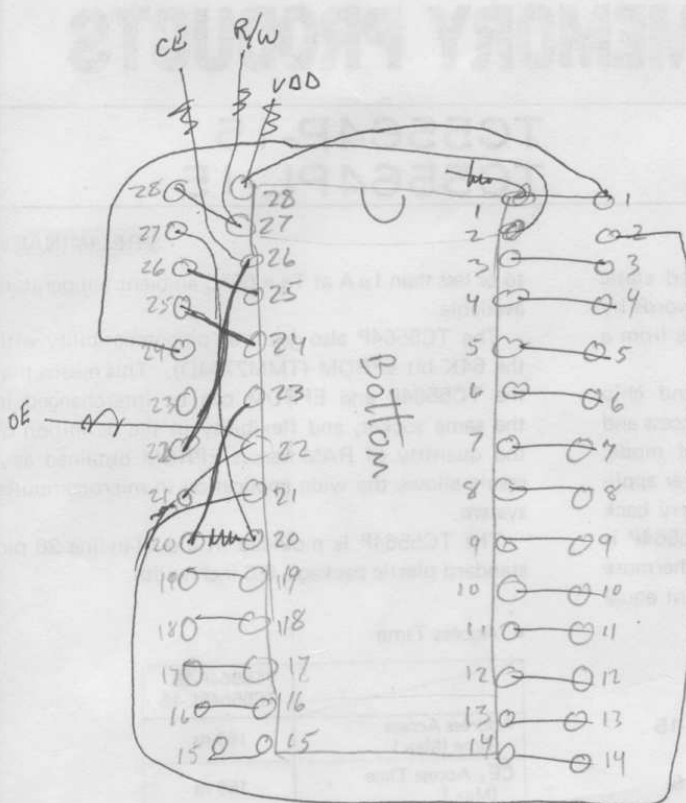|	TC5564P-15 TC5564PL-15
Address Access Time (Max.)	150 ns
$\overline{CE}_1$ Access Time (Max.)	150 ns
CE_2 Access Time (Max.)	150 ns
Output Enable Time (Max.)	70 ns

- Directly TTL Compatible:
  All Inputs and Outpus
- Stndard 28 Pin DIP
- Pin Compatible with 2764 type EPROM

BLOCK DIAGRAM

fold these pins under
1, 2, 20, 22, 23, 27, 28

— 209 —

Bottom

CE R/W VDD OE

A

Scan

By: Don Fox   sysop@atomicfrog.com

"It is better to have beer'd and fished, then
to have never loved before"